

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|------------------------------|------------------|---------|------------------|
| L1 | 6 | (SIMM and DIMM) same line same length | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:14 |
| L2 | 12 | (SIMM and DIMM) same load\$3 same line\$1 | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:14 |
| L3 | 28 | (SIMM DIMM) same load\$3 same line\$1 same (length long\$2 short\$2) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:15 |
| L4 | 599 | (SIMM DIMM) and memory and (load\$3 same line\$1) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:15 |
| L5 | 531 | (SIMM DIMM) and memory and (load\$3 same line\$1) and (length\$3 long\$2 short\$2) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:16 |
| L6 | 163 | (SIMM DIMM) and memory and (load\$3 same line\$1) same (length\$3 long\$2 short\$2) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:16 |
| L7 | 0 | (SIMM and DIMM) same "memory devices" and "impedance matching" | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:16 |
| L8 | 2 | (SIMM and DIMM) same "impedance matching" | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:17 |
| L9 | 24 | (SIMM and DIMM) and "impedance matching" same line\$1 | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:18 |
| L10 | 13 | (SIMM and DIMM) same delay\$3 same line\$1 | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:24 |
| L11 | 14 | (SIMM and DIMM) same delay\$3 same module\$1 | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:24 |
| L12 | 14 | (SIMM and DIMM) same delay\$3 same module\$1 | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:24 |
| L13 | 7 | (SIMM and DIMM) same delay\$3 and controller and (line\$1 same length) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/10/13 16:24 |